Customer Number: 20277

Case Docket Number: 60188-637 UTILITY PATENT APPLICATION UNDER 37 CFR 1.53(b)



Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Enclosed are:

Transmitted herewith for filing is the patent application of:

INVENTOR: Takashi YONEDA, Tsutomu KAMIYOSHI, Hiroshi BENNO, Shirou

YOSHIOKA, Tsuneo UENISHI

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT

10 pages of specification, claims, abstract. Declaration and Power of Attorney. Priority Claimed. Certified copy of Japanese Patent Application No. 2002-245218 3 sheets of formal drawing.

An assignment of the invention to and the assignment recordation fee.

Matsushita Electric Industrial Co., Ltd.

An associate power of attorney.

Information Disclosure Statement, Form PTO-1449 and reference.

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The filing fee has been calculated as shown below:

	NO. OF		EXTRA		
	CLAIMS		CLAIMS	RATE	AMOUNT
Total Claims	20	-20	0	\$18.00	\$0.00
Independent Claims	1	-3	0	\$84.00	\$0.00
			Multiple D	ependent Claim(s)	\$280.00
	Basic Fee				\$750.00
	Total of Above Calculations				\$1,030.00
			Less	\$0.00	
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Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: August 26, 2003